

FIG. 1

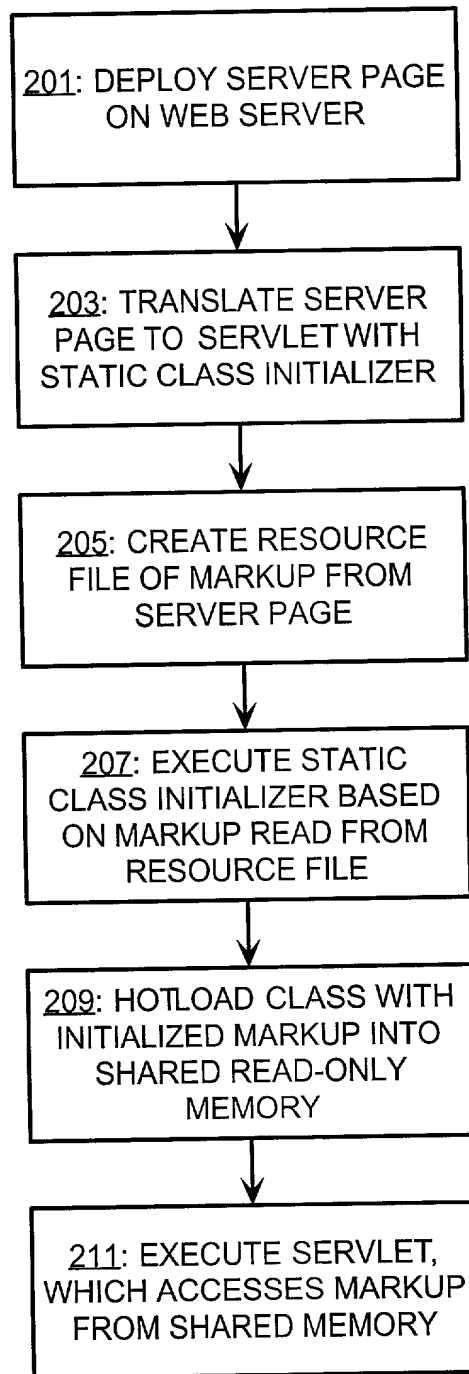


FIG. 2

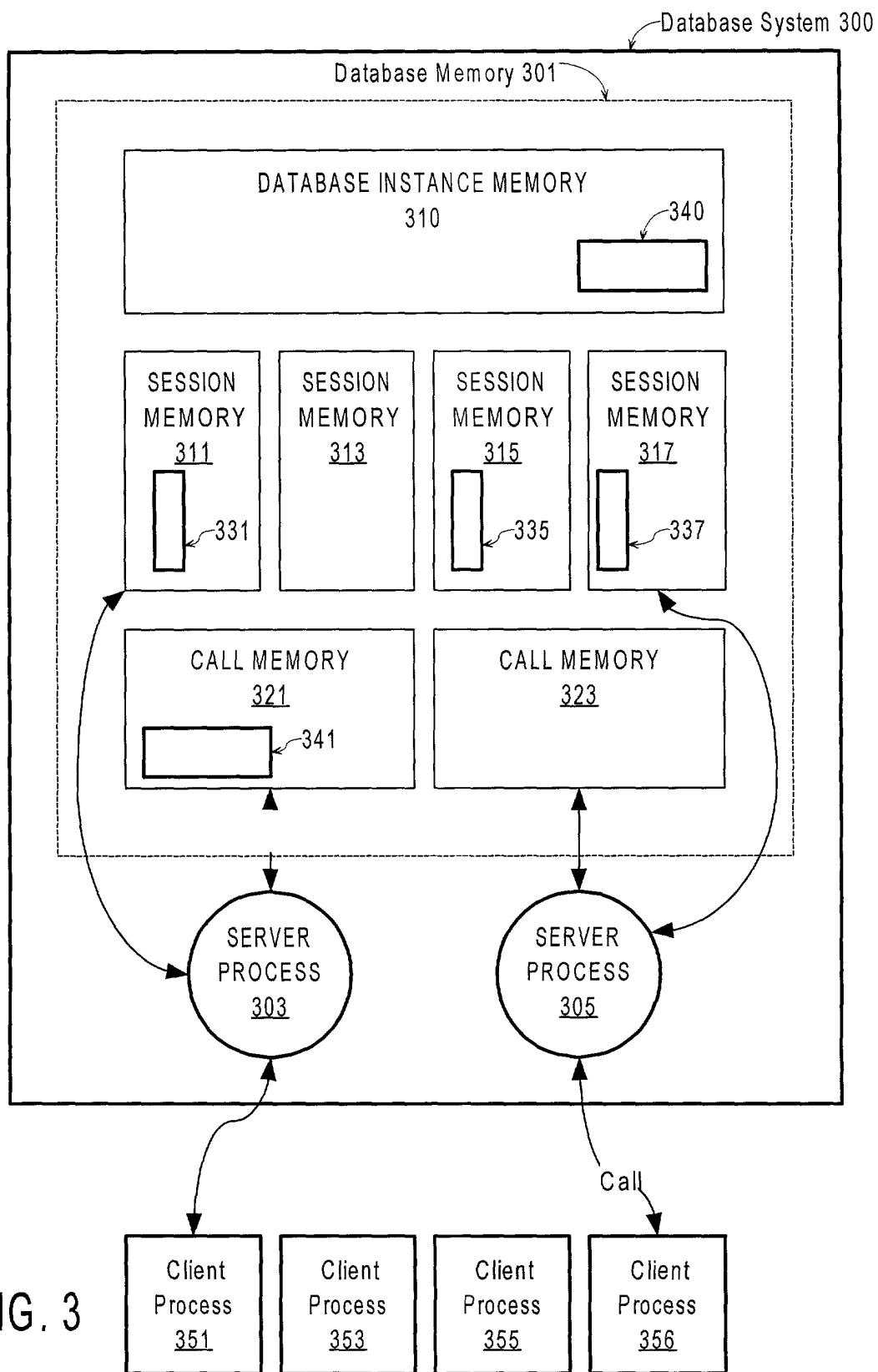


FIG. 3

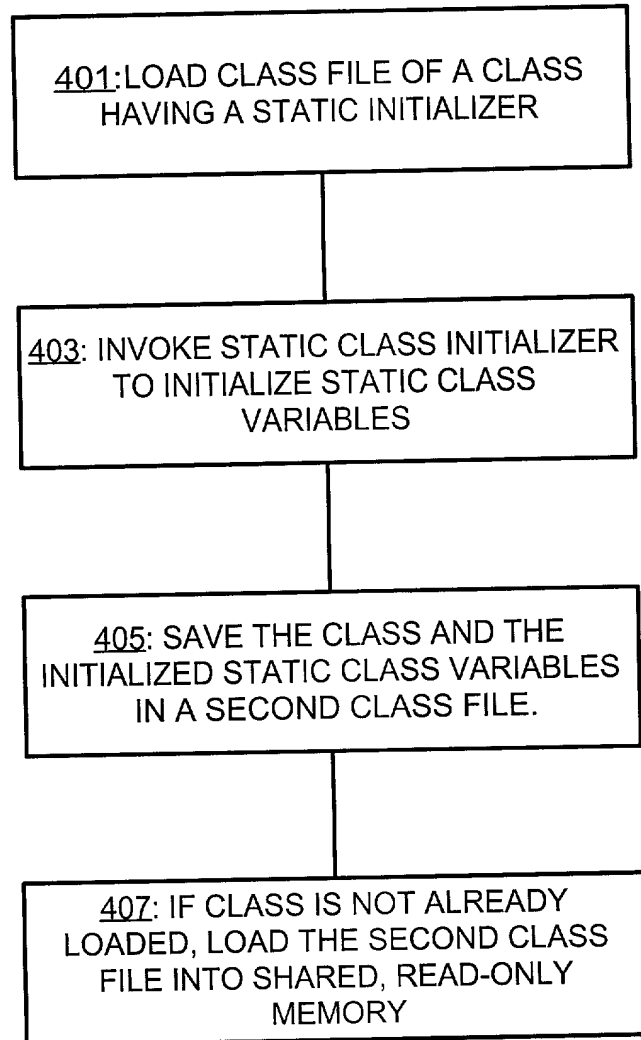


FIG. 4

FIG. 5 is a block diagram of a computer system 500. The system 500 includes a main memory 505, a ROM 507, a storage device 509, a processor 503, a bus 501, a communication interface 517, a display 511, an input device 513, and a cursor control 515. The main memory 505, ROM 507, and storage device 509 are connected to the bus 501. The processor 503 is also connected to the bus 501. The communication interface 517 is connected to the bus 501 and a network link 519. The display 511, input device 513, and cursor control 515 are also connected to the bus 501. The network link 519 is connected to a LAN 521, which is connected to a network 525. The LAN 521 is also connected to a host 523.

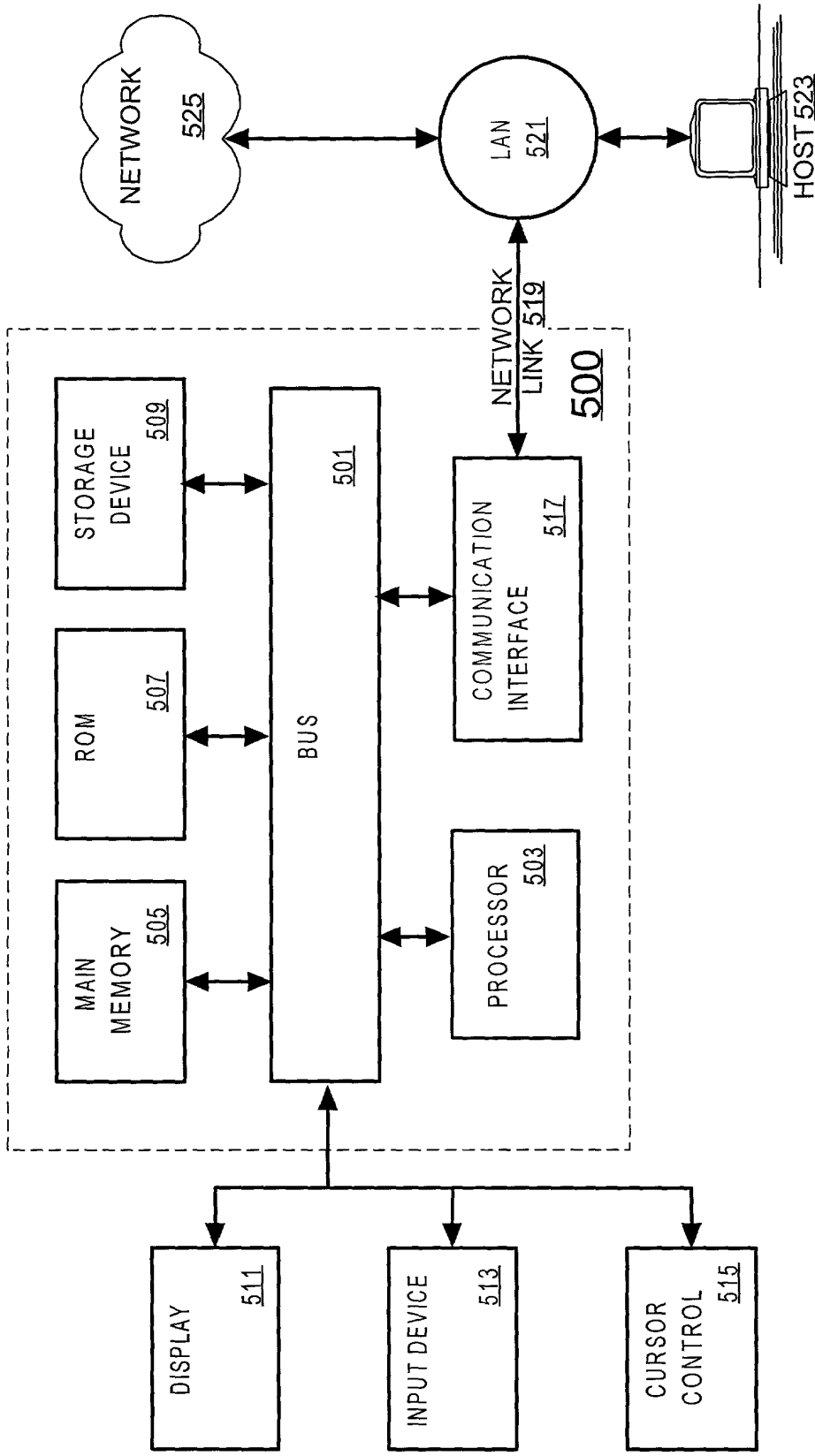


FIG. 5